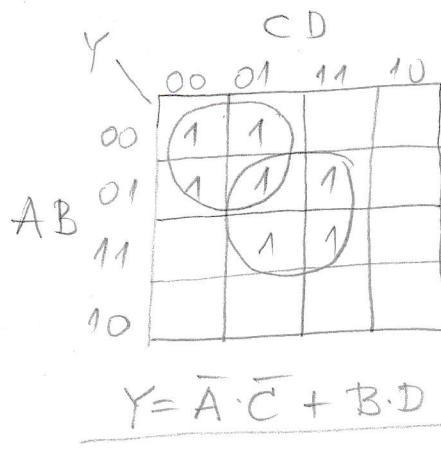
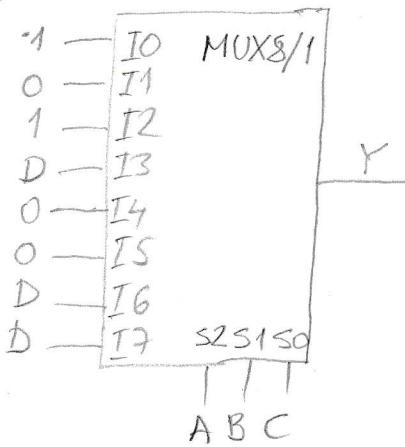
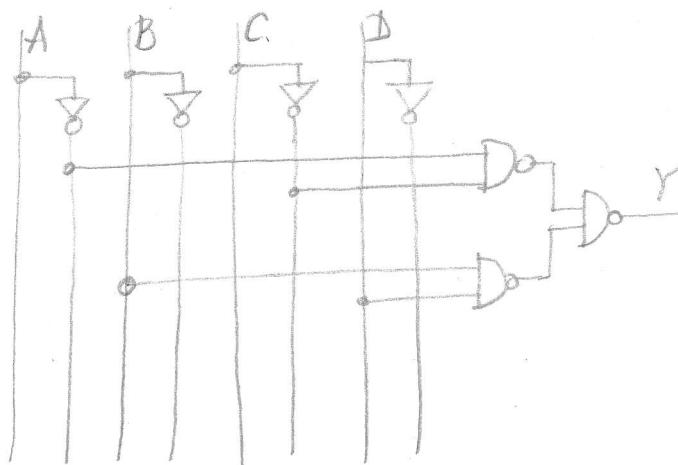


①

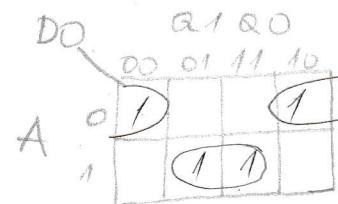
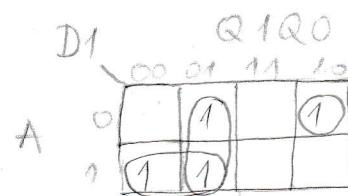
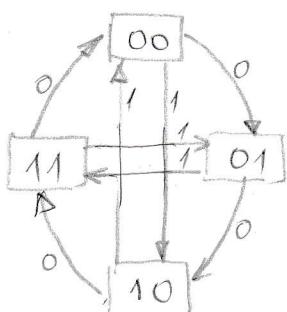


Truth table for the MUX8/1 circuit:

A	B	C	D	Y
0	0	0	0	1
0	0	0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	1



②

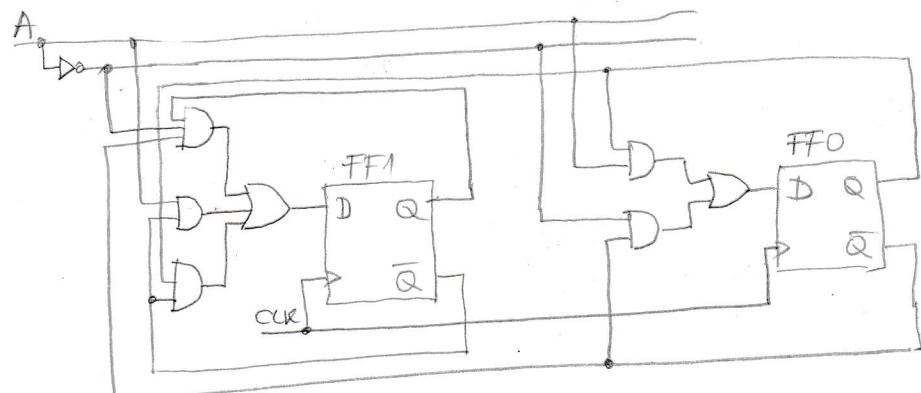


$$D1 = Q1(n+1) = A\bar{Q}1 + \bar{Q}1Q0 + \bar{A}Q1\bar{Q}0$$

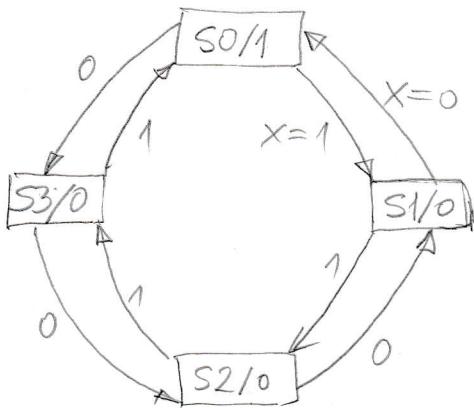
$$D0 = Q0(n+1) = \bar{A}\bar{Q}0 + A\bar{Q}0$$

Truth table for the state transition logic:

A	n	n+1		
	Q1	Q0	Q1	Q0
0	0	0	0	1
0	0	0	1	0
0	1	0	1	1
0	1	1	0	0
1	0	0	1	0
1	0	1	1	1
1	1	0	0	0
1	1	1	0	1



(3)

 $X=1 \rightarrow$  2 bit up-counter, Gray code $X=0 \rightarrow$  2 bit down-counter, Gray codeFor  $S_0 \rightarrow y = 1$ 

(4)

